

CMRDM7590**SURFACE MOUNT
DUAL P-CHANNEL
ENHANCEMENT-MODE
SILICON MOSFETS****ATTOmini™****SOT-963 CASE**• Device is **Halogen Free** by design**APPLICATIONS:**

- Load/Power Switches
- Power Supply Converter Circuits
- Battery Powered Portable Devices

MAXIMUM RATINGS: ($T_A=25^\circ\text{C}$)

Drain-Source Voltage
Gate-Source Voltage
Continuous Drain Current (Steady State)
Continuous Drain Current, $t_p \leq 5.0\text{s}$
Power Dissipation
Operating and Storage Junction Temperature
Thermal Resistance

www.centrasemi.com**DESCRIPTION:**

The CENTRAL SEMICONDUCTOR CMRDM7590 is an Enhancement-mode Dual P-Channel Field Effect Transistor designed for high speed pulsed amplifier and driver applications. This MOSFET offers Low $r_{DS(ON)}$ and Low Threshold Voltage.

MARKING CODE: CW**FEATURES:**

- Power Dissipation: 125mW
- Low Package Profile: 0.5mm (MAX)
- Low $r_{DS(ON)}$
- Low Threshold Voltage
- Logic Level Compatibility
- Small SOT-963 Surface Mount Package

SYMBOL		UNITS
V_{DS}	20	V
V_{GS}	8.0	V
I_D	140	mA
I_D	180	mA
P_D	125	mW
T_J, T_{stg}	-65 to +150	$^\circ\text{C}$
θ_{JA}	1000	$^\circ\text{C/W}$

ELECTRICAL CHARACTERISTICS PER TRANSISTOR: ($T_A=25^\circ\text{C}$ unless otherwise noted)

SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNITS
I_{GSSF}, I_{GSSR}	$V_{GS}=5.0\text{V}, V_{DS}=0$			100	nA
I_{DSS}	$V_{DS}=5.0\text{V}, V_{GS}=0$			50	nA
I_{DSS}	$V_{DS}=16\text{V}, V_{GS}=0$			100	nA
BV_{DSS}	$V_{GS}=0, I_D=250\mu\text{A}$	20			V
$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	0.4		1.0	V
$r_{DS(ON)}$	$V_{GS}=4.5\text{V}, I_D=100\text{mA}$		4.0	5.0	Ω
$r_{DS(ON)}$	$V_{GS}=2.5\text{V}, I_D=50\text{mA}$		5.5	7.0	Ω
$r_{DS(ON)}$	$V_{GS}=1.8\text{V}, I_D=20\text{mA}$		8.0	10	Ω
$r_{DS(ON)}$	$V_{GS}=1.5\text{V}, I_D=10\text{mA}$		11	17	Ω
$r_{DS(ON)}$	$V_{GS}=1.2\text{V}, I_D=1.0\text{mA}$		20		Ω
$Q_g(\text{tot})$	$V_{DS}=10\text{V}, V_{GS}=4.5\text{V}, I_D=100\text{mA}$		0.536		nC
Q_{gs}	$V_{DS}=10\text{V}, V_{GS}=4.5\text{V}, I_D=100\text{mA}$		0.292		nC
Q_{gd}	$V_{DS}=10\text{V}, V_{GS}=4.5\text{V}, I_D=100\text{mA}$		0.236		nC
g_{FS}	$V_{DS}=5.0\text{V}, I_D=125\text{mA}$		1.3		S
C_{rss}	$V_{DS}=15\text{V}, V_{GS}=0, f=1.0\text{MHz}$		1.0		pF
C_{iss}	$V_{DS}=15\text{V}, V_{GS}=0, f=1.0\text{MHz}$		12		pF
C_{oss}	$V_{DS}=15\text{V}, V_{GS}=0, f=1.0\text{MHz}$		2.7		pF
t_{on}	$V_{DD}=10\text{V}, V_{GS}=4.5\text{V}, I_D=200\text{mA}$		60		ns
t_{off}	$V_{DD}=10\text{V}, V_{GS}=4.5\text{V}, I_D=200\text{mA}$		210		ns

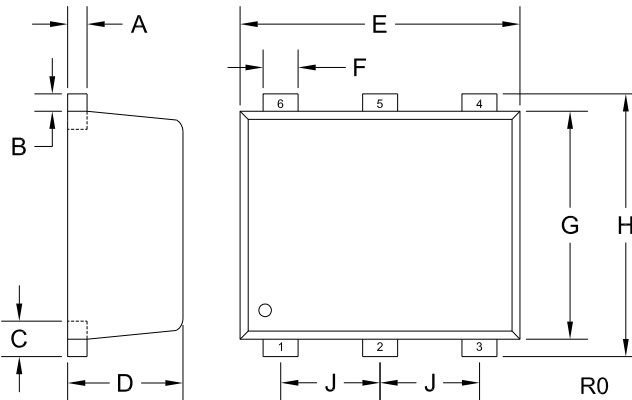
R2 (2-August 2011)

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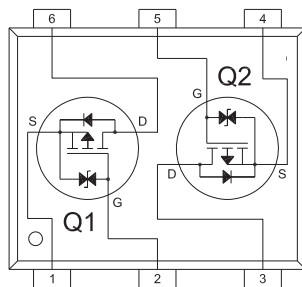
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DUAL P-CHANNEL
ENHANCEMENT-MODE
SILICON MOSFETS



SOT-963 CASE - MECHANICAL OUTLINE



PIN CONFIGURATION



LEAD CODE:

- 1) Source Q1
- 2) Gate Q1
- 3) Drain Q2
- 4) Source Q2
- 5) Gate Q2
- 6) Drain Q1

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SYMBOL	DIMENSIONS			
	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.002	0.006	0.050	0.150
B	0.002	0.006	0.050	0.150
C	0.005	0.007	0.125	0.175
D	0.016	0.020	0.400	0.500
E	0.037	0.041	0.950	1.050
F	0.004	0.008	0.100	0.200
G	0.030	0.033	0.750	0.850
H	0.037	0.041	0.950	1.050
J	0.014		0.350	

SOT-963 (REV: R0)

R2 (2-August 2011)